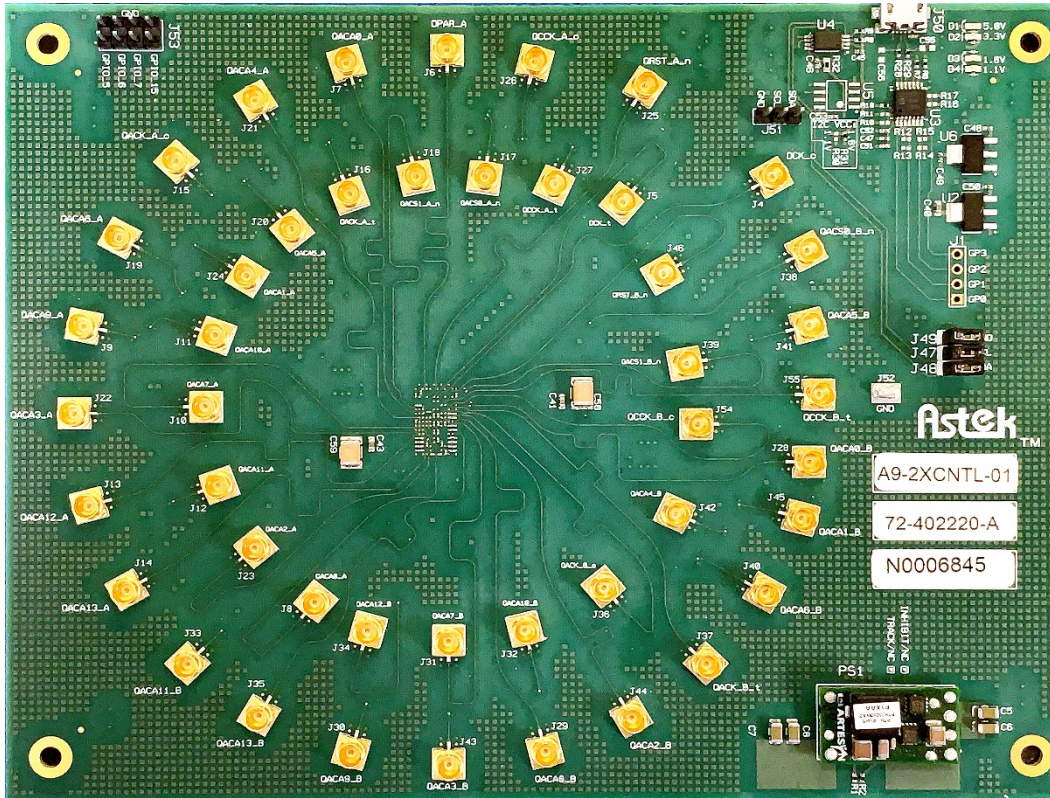


A9-Wide Controller

Product Brief



Transforming Technology into Customer Value



DDR5 WIDE CONTROLLER BOARD

The DDR5 Wide Controller Board provides DRAM/DIMM (UDIMM) testing signals. Utilizing the Virtual Host Mode (VHOST) of the Registering Clock Driver (RCD), the Wide Controller Board provides, CA, CS_n, and Clocks signals from both Channels A and B of the RCD to the device under test. In addition, the A9-Controller Software package included with the Controller Board provides a command line interface to issue test sequences to the device under test.

Features

- Issues DRAM commands to CA using RCD VHOST mode
- Interface directly to A9-CTC2 via SMP to SMP cables

Configuration

- A9-WCNTL-01 Wide Controller Board
- A9-CTC-01 DDR5 Single-Slot CTC2 Board
- 34 SMP to SMP cables for CA signals
- 8 SMP to SMP cables (match pair) for Clock output

Ordering Information

Model Number	Product Description
A9-WCNTL-01	Wide Controller Board

For more information, contact your
Keysight sales representative or visit
the Astek web site at:

www.astekcorp.com