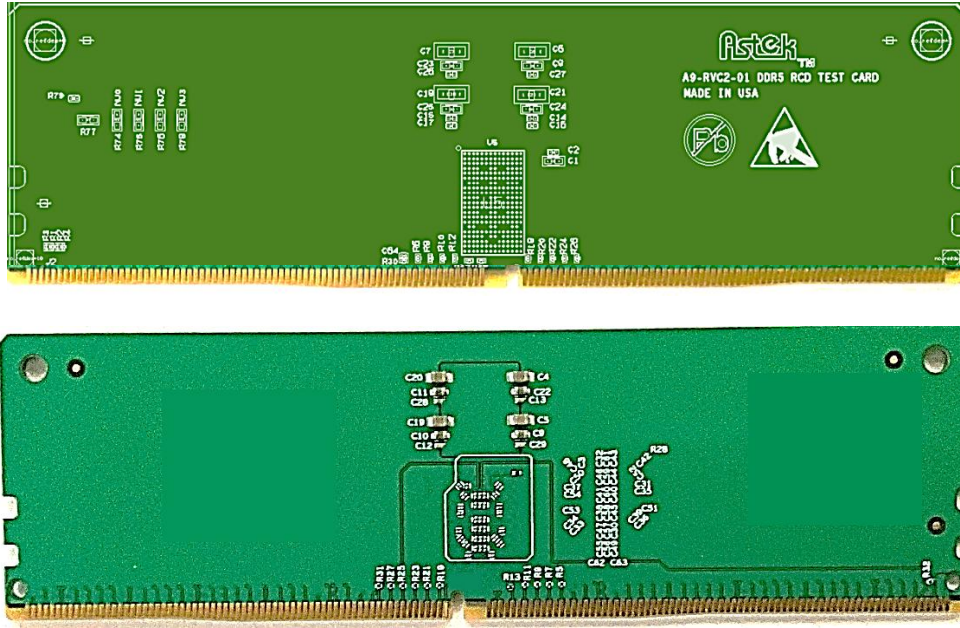


A9-RVC2-01

User Guide



Transforming Technology
into Customer Value



DDR5 RVC2 TEST CARD

A versatile DIMM test card focused on Gen5 Registering Clock Driver (RCD05) test. Provides access to all I/O's DCA_A/B, Q(A/B)CA_A and Q(A/B)CA_B inputs and outputs of the RCD05 under test. A9-RVC2-01 Test Card is configured over I2C using CTC2 Controller software. Compatible with Astek's A9-CTC2 Channel Test Card.

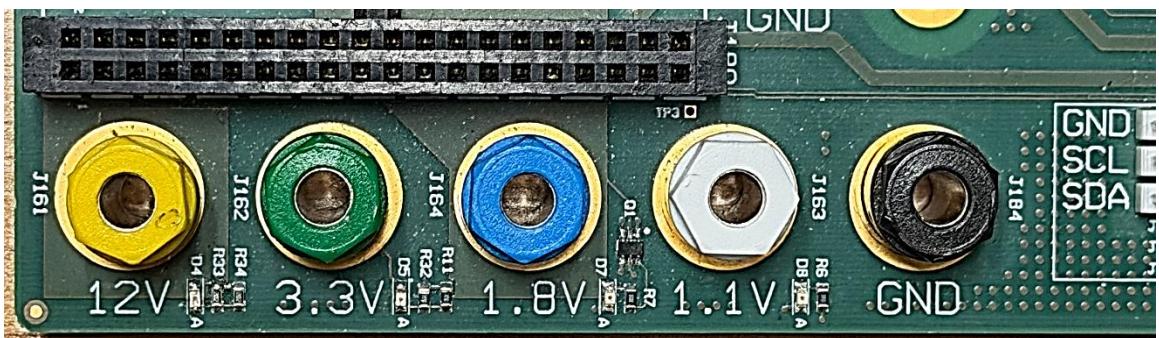
Features

- A9-RVC2-01 inputs and outputs DCA[6:0]_A/B, Q(A/B)CA[13:0]_A/B, QACS[3:0]_A/B, Q[A:Z]CK, DNU(0:13), DCK, interfaced to CTC DIMM connector
- RFU pins routed to Gold Fingers for user specific tests
- 287 Terminal Gold Finger Edge Connector Complies with JEDEC SO-23E
- Control of RVC2 by CTC2 Controller software

Power Supply

CTC2 Terminal	Voltage
1.1V/Vin_BULK (Yellow)	1.1V
3.3V/Vin_MGMT (Red)	1.8 V

Connection



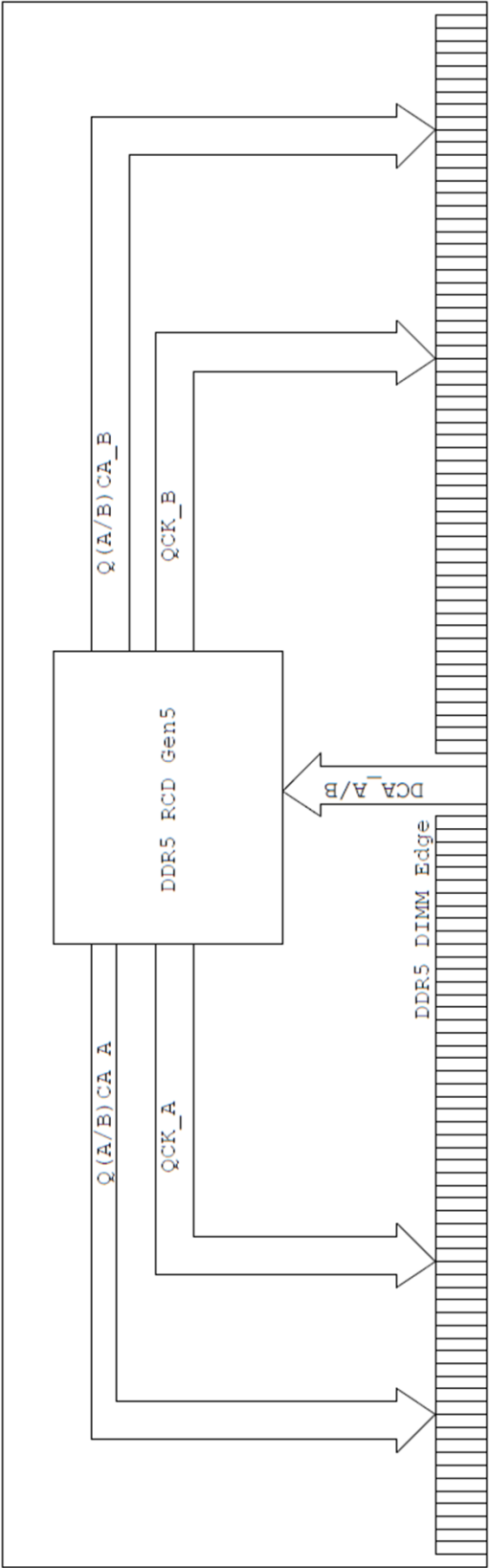
CTC2 Power Terminals

CTC3 Terminal	Voltage
Vin_BULK (Yellow)	1.1V
Vin_MGMT (Red)	1.8 V



CTC3 Power Terminals

Block Diagram



RCD Signals to CTCx DIMM Fingers/SMP

RVC2		
RCD05 Signal	DIMM pin	CTC2 Signal
QBCA10_A	7	DQ0_A
QBCA11_A	9	DQ1_A
QBCA12_A	152	DQ2_A
QBCA13_A	154	DQ3_A
QBCA3_A	14	DQ4_A
QBCA1_A	16	DQ5_A
QBCA7_A	159	DQ6_A
QBCA8_A	161	DQ7_A
QBCA9_A	18	DQ8_A
QBCA5_A	20	DQ9_A
QBCA6_A	163	DQ10_A
QBCA4_A	165	DQ11_A
QBCA2_A	25	DQ12_A
QBCA0_A	27	DQ13_A
QBCS2_A	170	DQ14_A
QBCS3_A	172	DQ15_A
QBCS1_A	29	DQ16_A
QBCS0_A	31	DQ17_A
QACA11_A	174	DQ18_A
QACA13_A	176	DQ19_A
QACA12_A	36	DQ20_A
QACA10_A	38	DQ21_A
QACA9_A	181	DQ22_A
QACA3_A	183	DQ23_A
QACA7_A	40	DQ24_A
QACA2_A	42	DQ25_A
QACA6_A	185	DQ26_A
QACA8_A	187	DQ27_A
QACA4_A	47	DQ28_A
QACA5_A	49	DQ29_A
QACA1_A	192	DQ30_A
QACA0_A	194	DQ31_A

RVC2		
RCD05 Signal	DIMM pin	CTC2 Signal
QACA5_B	100	DQ0_B
QACA0_B	102	DQ1_B
QACA4_B	245	DQ2_B
QACA1_B	247	DQ3_B
QACA2_B	107	DQ4_B
QACA8_B	109	DQ5_B
QACA7_B	252	DQ6_B
QACA6_B	254	DQ7_B
QACA10_B	111	DQ8_B
QACA12_B	113	DQ9_B
QACA3_B	256	DQ10_B
QACA9_B	258	DQ11_B
QACA11_B	118	DQ12_B
QACA13_B	120	DQ13_B
QBCS3_B	263	DQ14_B
QBCS2_B	265	DQ15_B
QBCS0_B	122	DQ16_B
QBCS1_B	124	DQ17_B
QBCA4_B	267	DQ18_B
QBCA0_B	269	DQ19_B
QBCA5_B	129	DQ20_B
QBCA2_B	131	DQ21_B
QBCA8_B	274	DQ22_B
QBCA1_B	276	DQ23_B
QBCA6_B	133	DQ24_B
QBCA9_B	135	DQ25_B
QBCA3_B	278	DQ26_B
QBCA7_B	280	DQ27_B
QBCA13_B	140	DQ28_B
QBCA12_B	142	DQ29_B
QBCA11_B	285	DQ30_B
QBCA10_B	287	DQ31_B

RCD Signals to CTCx DIMM Fingers/SMP

RVC2		
RCD05 Signal	DIMM pin	CTC2 Signal
QBCK_A_t	11	DQS0_A_t
QBCK_A_c	12	DQS0_A_c
QDCK_A_t	22	DQS1_A_t
QDCK_A_c	23	DQS1_A_c
QWCK_A_t	33	DQS2_A_t
QWCK_A_c	34	DQS2_A_c
QACK_A_t	44	DQS3_A_t
QACK_A_c	45	DQS3_A_c
DNU4	55	DQS4_A_t
DNU3	56	DQS4_A_c
QZCK_A_t	156	DQS5_A_T
QZCK_A_c	157	DQS5_A_c
QXCK_A_t	167	DQS6_A_t
QXCK_A_c	168	DQS6_A_c
QYCK_A_t	178	DQS7_A_t
QYCK_A_c	179	DQS7_A_c
QCCK_A_t	189	DQS8_A_t
QCCK_A_c	190	DQS8_A_c
DLBD_A	201	DQS9_A_T
DLBS_A	200	DQS9_A_c

DCS0_Q	64	CS0_A
DCS1_Q	209	CS1_A
DCA0_A	66	CA0_A
DCA1_A	211	CA1_A
DCA2_A	68	CA2_A
DCA3_A	213	CA3_A
DCA4_A	70	CA4_A
DCA5_A	215	CA5_A
DCA6_A	72	CA6_A
DPAR_A	74	PAR_A

HSCL	4	SCL
HSDA	5	SCA
ALERT	62	ALERT
RESET	207	RESET

RVC2		
RCD05 Signal	DIMM pin	CTC2 Signal
QCCK_B_c	105	DQS0_B_c
QCCK_B_t	104	DQS0_B_c
QYCK_B_c	116	DQS1_B_t
QYCK_B_t	115	DQS1_B_c
QXCK_B_c	127	DQS2_B_t
QXCK_B_t	126	DQS2_B_c
QZCK_B_c	138	DQS3_B_t
QZCK_B_t	137	DQS3_B_c
DNU10	94	DQS4_B_t
DNU11	93	DQS4_B_c
QACK_B_c	249	DQS5_B_t
QACK_B_t	250	DQS5_B_c
QWCK_B_c	260	DQS6_B_t
QWCK_B_t	261	DQS6_B_c
QDCK_B_c	271	DQS7_B_t
QDCK_B_t	272	DQS7_B_c
QBCK_B_c	282	DQS8_B_t
QBCK_B_t	283	DQS8_B_c
DLBD_B	238	DQS9_B_t
DLBS_B	239	DQS9_B_c

DCS0_B	76	CS0_B
DCS1_B	221	CS1_B
DCA0_B	78	CA0_B
DCA1_B	223	CA1_B
DCA2_B	80	CA2_B
DCA3_B	225	CA3_B
DCA4_B	82	CA4_B
DCA5_B	227	CA5_B
DCA6_B	84	CA6_B
DPAR_B	229	PAR_B

SAA	148	J179
DCK_T	217	CK_t
DCK_c	218	CK_c

RCD Signals to CTCx DIMM Fingers/SMP

RVC2		
RCD05 Signal	DIMM pin	CTC2 Signal
QACS1_A	51	CB0_A
QACS3_A	53	CB1_A
QACS0_A	196	CB2_A
DNU2	198	CB3_A
DNU0	58	CB4_A
DNU1	60	CB5_A
QACS2_A	203	CB6_A
DNU6	205	CB7_A

DERROR_IN_A	147	FAIL J173
NU[1]	2	PWR_GOOD
RFU_1	144	RFU_0
FU_2	149	J98
RFU_4	231	RFU_4
RFU_5	232	RFU_5

DNU7	89	CB0_B
DNU8	91	CB1_B
DNU13	234	CB2_B
QACS3_B	236	CB3_B
QACS2_B	96	CB4_B
QACS1_B	98	CB5_B
DNU9	241	CB6_B
QACS0_B	243	CB7_B

QLBD	86	LBD
QLBS	87	LBS

Configuration

- All critical passive Rs and Cs installed
- RCD05 IC not provided by customer

Ordering Information

Model Number	Product Description
A9-RVC2-01	RVC2 Test Card

For more information, contact your Keysight sales representative or visit the Astek web site at:
www.astekcorp.com